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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CJD200 and CJD210 are complementary silicon power transistors manufactured in a surface mount package designed for high current amplifier applications.

MARKING: FULL PART NUMBER

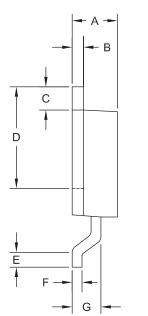
MAXIMUM RATINGS: (T_C=25°C unless otherwise noted)

		SYMBOL		UNITS
Collector-Base Voltage		V _{CBO}	40	V
Collector-Emi	itter Voltage	VCEO	25	V
Emitter-Base	Voltage	V _{EBO}	8.0	V
Continuous C	Collector Current	Ι _C	5.0	А
Peak Collector Current		ICM	10	А
Continuous Base Current		۱ _B	1.0	А
Power Dissip	ation	PD	12.5	W
Power Dissip	ation (T _A =25°C)	PD	1.4	W
Operating and Storage Junction Temperature		TJ, Tstg	-65 to +150	°C
Thermal Resistance		ΘJC	10	°C/W
Thermal Resistance		Θ_{JA}	89.3	°C/W
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				UNITS
ICBO	V _{CB} =40V		100	nA
ICBO	V _{CB} =40V, T _C =125°C		100	μA
			100	nA
BVCEO	I _C =10mA	25		V
V _{CE} (SAT)	I _C =500mA, I _B =50mA		0.3	V
VCE(SAT)	I _C =2.0A, I _B =200mA		0.75	V
VCE(SAT)	I _C =5.0A, I _B =1.0A		1.8	V
V _{BE} (SAT)	I _C =5.0A, I _B =1.0A		2.5	V
V _{BE(ON)}	V _{CE} =1.0V, I _C =2.0A		1.6	V
h _{FE}	V _{CE} =1.0V, I _C =500mA	70		
h _{FE}	V _{CE} =1.0V, I _C =2.0A	45	180	
h _{FE}	V _{CE} =2.0V, I _C =5.0A	10		
fT	V _{CE} =10V, I _C =100mA, f=10MHz	65		MHz
Cob	V _{CB} =10V, I _E =0, f=0.1MHz (CJD20	00)	80	pF
Cob	V_{CB} =10V, I _E =0, f=0.1MHz (CJD2 ⁻	10)	120	pF

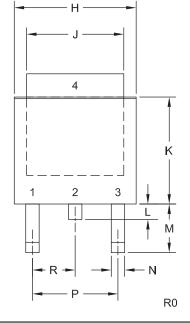
R3 (21-January 2013)



SURFACE MOUNT SILICON COMPLEMENTARY POWER TRANSISTORS



DPAK CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Base
- 2) Collector
- 3) Emitter
- 4) Collector
- MARKING:

FULL PART NUMBER

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A	0.083	0.108	2.10	2.75			
В	0.016	0.032	0.40	0.81			
С	0.035	0.063	0.89	1.60			
D	0.203	0.228	5.15	5.79			
E	0.020	-	0.51	-			
F	0.018	0.024	0.45	0.60			
G	0.051	0.071	1.30	1.80			
Н	0.248	0.268	6.30	6.81			
J	0.197	0.217	5.00	5.50			
K	0.209	0.245	5.30	6.22			
L	0.025	0.040	0.64	1.02			
М	0.090	0.115	2.30	2.91			
N	0.012	0.045	0.30	1.14			
Р	0.180		4.60				
R	0.090		2.30				
DPAK (REV: R0)							

R3 (21-January 2013)

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- Custom bar coding for shipments
- Custom product packing

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- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

CONTACT US

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